

Abstract of the Disclosure

An integrated circuit device externally connected with a peripheral device operated by a first clock signal. The integrated circuit device includes a CPU having information on the frequency of the first clock signal, a clock generator generating a second clock signal for operating the CPU and outputting third clock signals obtained from the second clock signal and a clock halt portion receiving the third clock signals and selectively outputting only one of the third clock signals according to the information. The integrated circuit device further includes a timer activated only when receiving the one of the third clock signals and converting the frequency of the received clock signal for output and a clock synchronization serial port receiving the clock signal outputted from the timer and one of the other third clock signals, and supplying either one of the received clock signals to the peripheral device according to the information.